

SSC8169GS6

P-Channel Enhancement Mode MOSFET

Features

V _{DS}	V _{GS}	R _{DS(ON)} Typ.	l _D
-60V	±20V	155mΩ@-10V	-2.1A
		185mΩ@-4V5	-2.1A

Description

The This device is produced with high cell density DMOS trench technology, which is especially used to minimize on-state resistance. This device particularly suits low voltage applications such as portable equipment, power management and other battery powered circuits, and low in-line power dissipation are needed in a very small outline surface mount package.

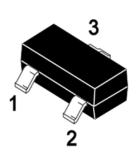
Applications

- Load Switch
- Portable Devices
- DCDC Conversion

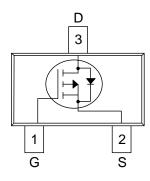
Ordering Information

Device	Package	Shipping	
SSC8169GS6	SOT-23	3000/Reel	

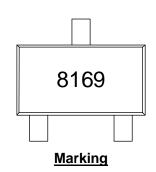
Pin configuration



SOT-23



Pin Configuration (Top View)





➤ Absolute Maximum Ratings (T_A=25°C unless otherwise noted)

Symbol	Parameter	Ratings	Unit
V _{DSS}	Drain-to-Source Voltage	-60	V
V _{GSS}	Gate-to-Source Voltage	±20	V
I _D	Continuous Drain Current a	-2.1	Α
I _{DM}	Pulsed Drain Current b	-8	Α
P _D	Power Dissipation ^a	1.14	W
TJ	Operation junction temperature	-55~150	$^{\circ}$
T _{STG}	Storage temperature range	-55~150	$^{\circ}$ C

➤ Thermal Resistance Ratings (T_A=25°C unless otherwise noted)

Symbol	Parameter	Maximum	Unit
Reja	Junction-to-Ambient Thermal Resistance a	110	°C/W

Note:

- a. The value of R_{θJA} is measured with the device mounted on 1 in² FR-4 board with 2oz.copper, in a still air environment with T_A=25°C. The value in any given application depends on the user is specific board design. The power dissipation is based on the t≤10s thermal resistance rating.
- b. Repetitive rating, pulse width limited by junction temperature.



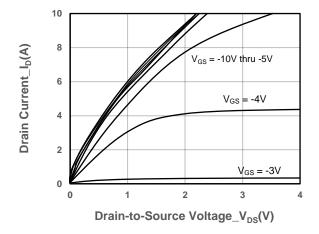


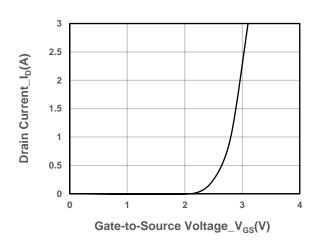
\succ Electrical Characteristics (T_A=25°C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0V, I_{D} = -250\mu A$	-60			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = -250uA$	-1	-2	-3	V
Drain Course On Registeres	R _{DS(on)}	$V_{GS} = -10V, I_D = -1A$		155	215	mΩ
Drain-Source On-Resistance		V _{GS} = -4.5V, I _D = -1A		185	260	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -60V, V _{GS} = 0V			-1	μA
Gate-Source Leak Current	Igss	V _{GS} = ±20V, V _{DS} = 0V			±100	nA
Forward Voltage	V _{SD}	V _{GS} = 0V, I _S = -1.2A		-0.8	-1.3	V
Input Capacitance	Ciss			510		pF
Output Capacitance	Coss	$V_{DS} = -30V$, $V_{GS} = 0V$, $f = 1MHz$		31		
Reverse Transfer Capacitance	Crss	I = IIVIDZ		21		
Turn-on Delay Time	T _{D(ON)}	V_{DS} = -30V, I_{D} = -1.2A, R_{L} = 6 Ω , V_{GS} = -10V, R_{G} = 3 Ω		7.2		ns
Rise Time	Tr			21		
Turn-off Delay Time	T _{D(OFF)}			37		
Fall Time	T _f			12		
Total Gate Charge	Q _G	$V_{DS} = -30V, V_{GS} = -10V,$ $I_{D} = -1.2A$		12		
Gate to Source Charge	Q _{GS}			2.1		nC
Gate to Drain Charge	Q _{GD}	ID = -1.2A		2.4		



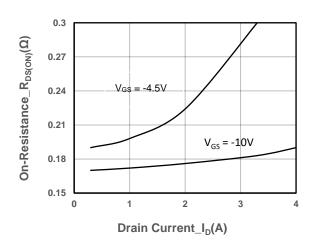
> Typical Performance Characteristics (T_A=25℃ unless otherwise noted)

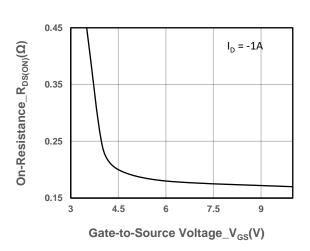




Output Characteristics

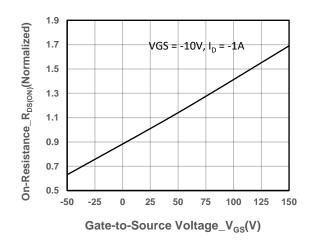
Transfer Characteristics

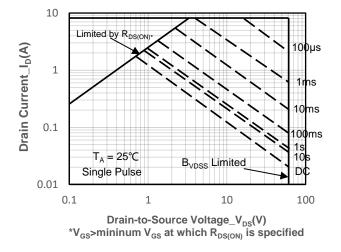




On-Resistance vs. Drain Current and Gate Voltag

On-Resistance vs. Gate-to-Source Voltage



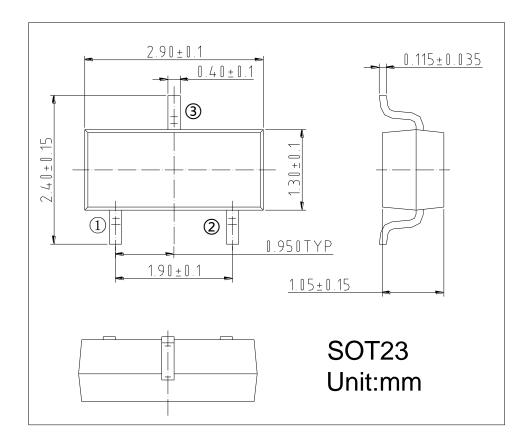


On-Resistance vs. Junction Temperature

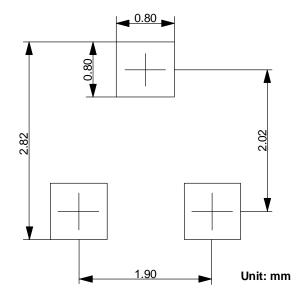
Safe Operating Area vs. Junction-to-Ambient



Package Information



Suggested Pad Layout





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